

Mata kuliah: **Teknik Otomasi**

# Sequential Function Charts

**Eka Maulana, ST, MT, MEng.**

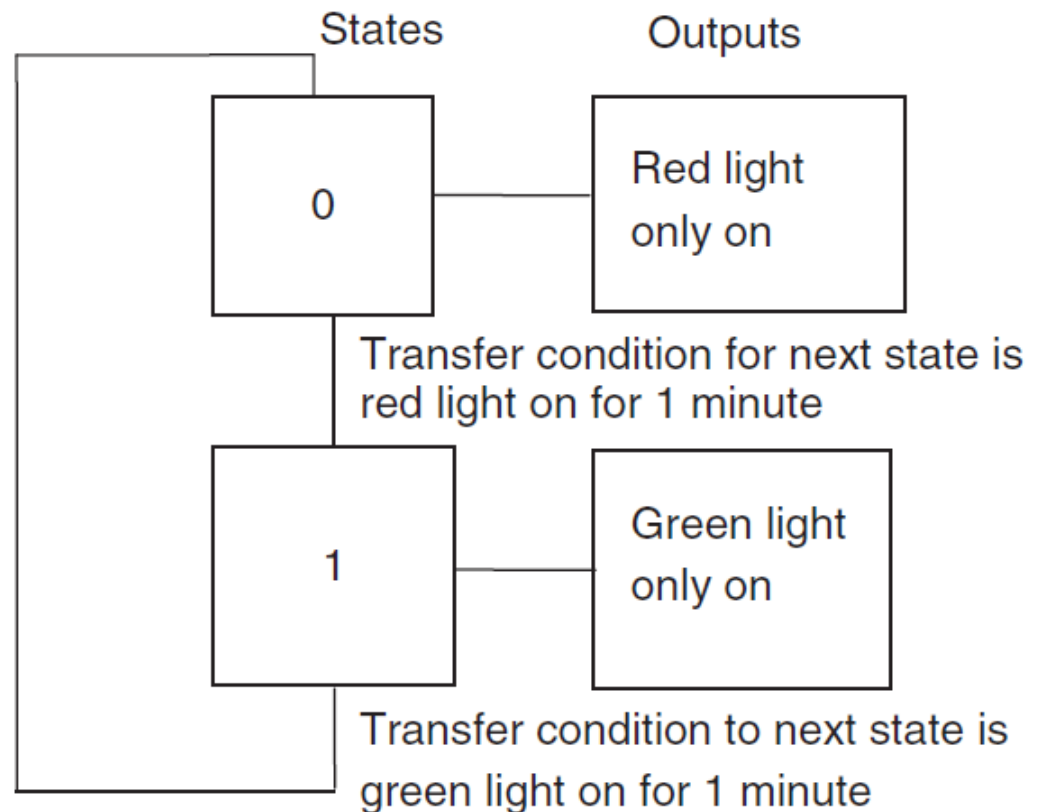
Department of Electrical Engineering

Brawijaya University

# Sequential Function Charts (SFC)

Used for a **pictorial representation** of a **system's operation** to show the **sequence of events** involved in its operation

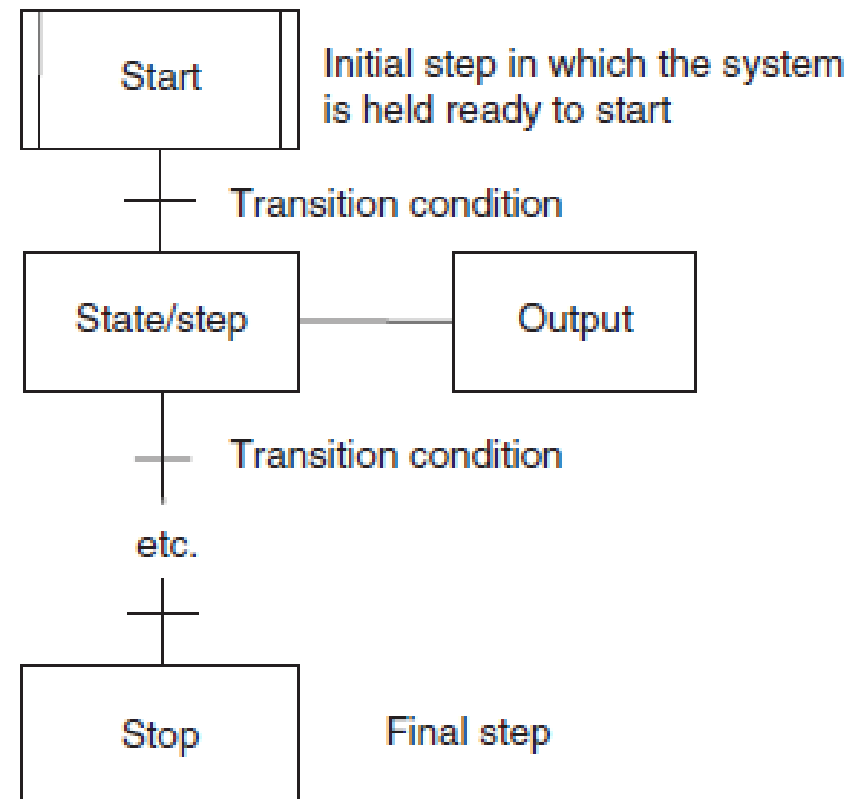
*Ex: SFC of Traffic Light*



# Element of SFC

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- The operation is described by a **number of separate sequentially** connected states or **steps** that are represented by **rectangular boxes**, **each representing a particular state** of the system being controlled.
- The **initial step** in a program is **represented differently from the other steps**.



# Element of SFC

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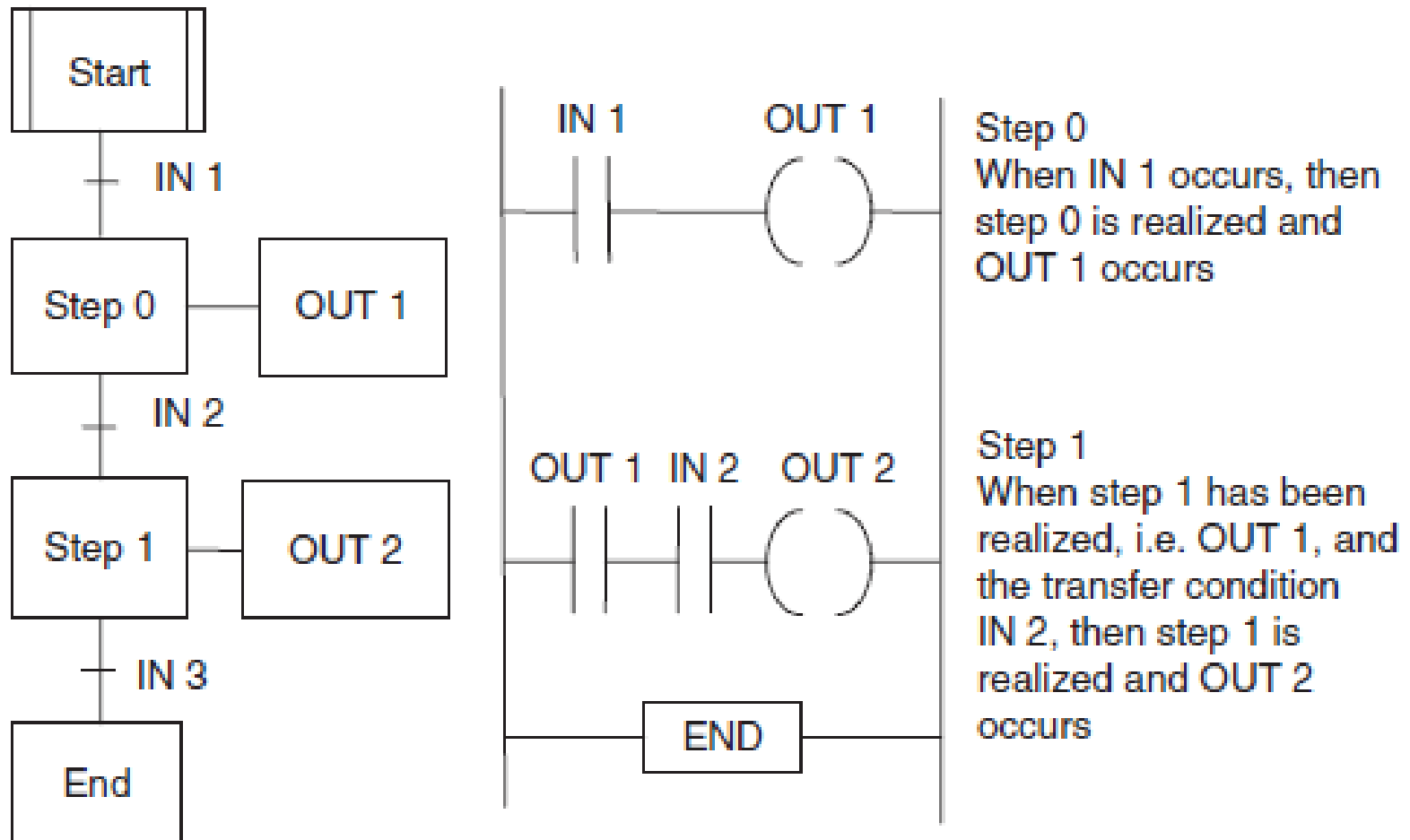
- Each connecting line between **states** has a **horizontal bar** representing the **transition condition** that has to be realized before the system can move from one state to the next.
- **Two steps can never be directly connected**; they must always be separated by a transition.
- **Two transitions can never directly follow from one to another**; they must always be separated by a step.

# Element of SFC

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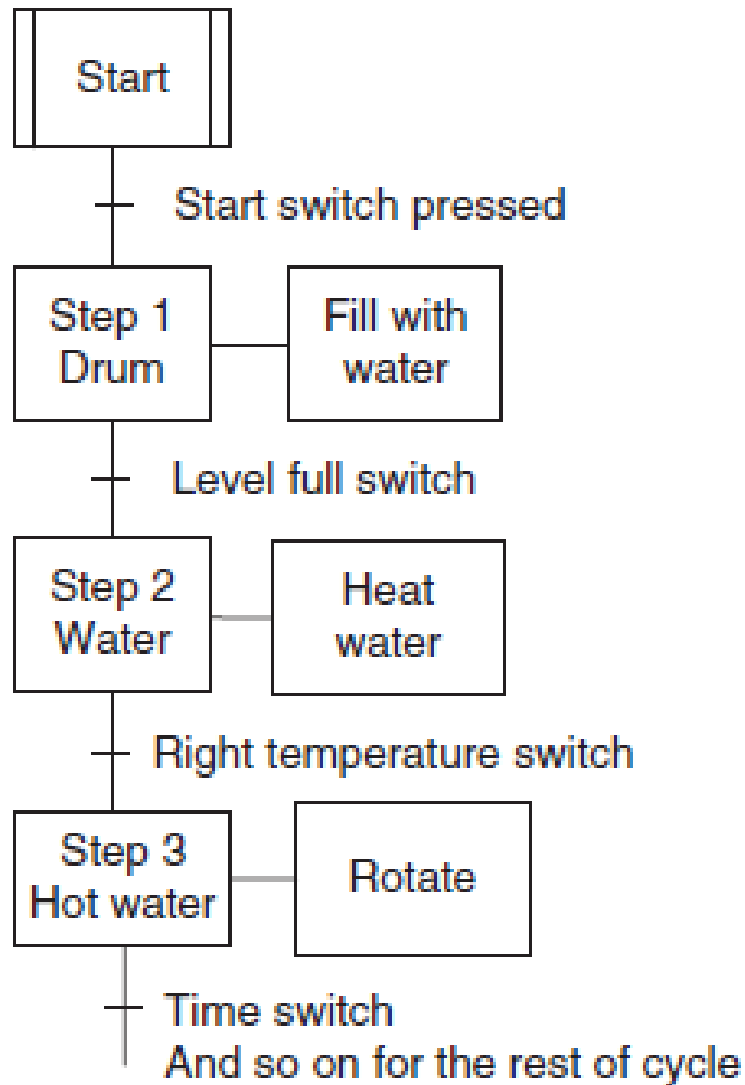
- When the transfer conditions to the next state are realized, the **next state or step in the program occurs**.
- The process thus continues from one state to the next until the **complete machine cycle is completed**.
- **Outputs/actions** at any state are represented by **horizontally linked boxes** and **occur when that state has been realized**.

# SFC and Equivalen Ladder Diagram



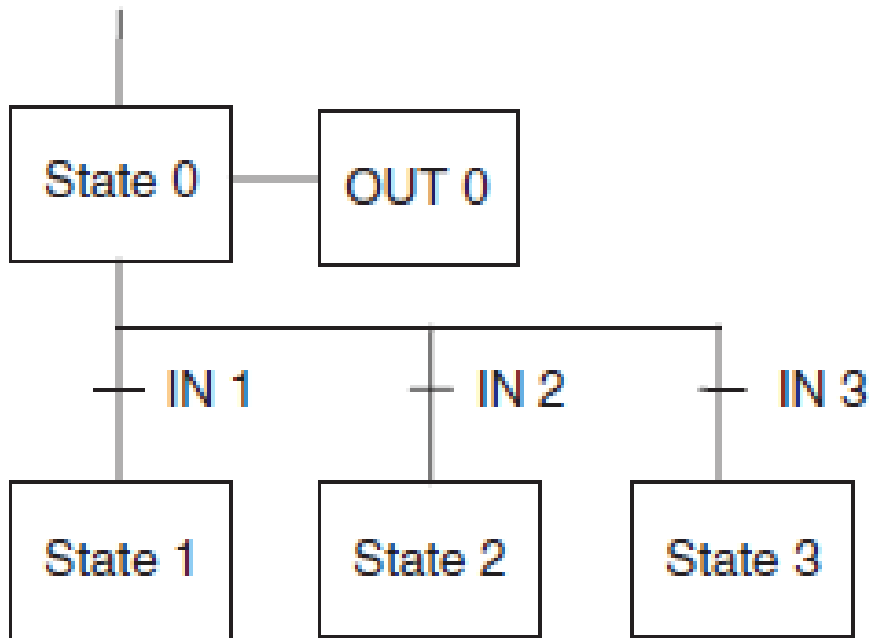
# Ex. SFC of Washing Machine

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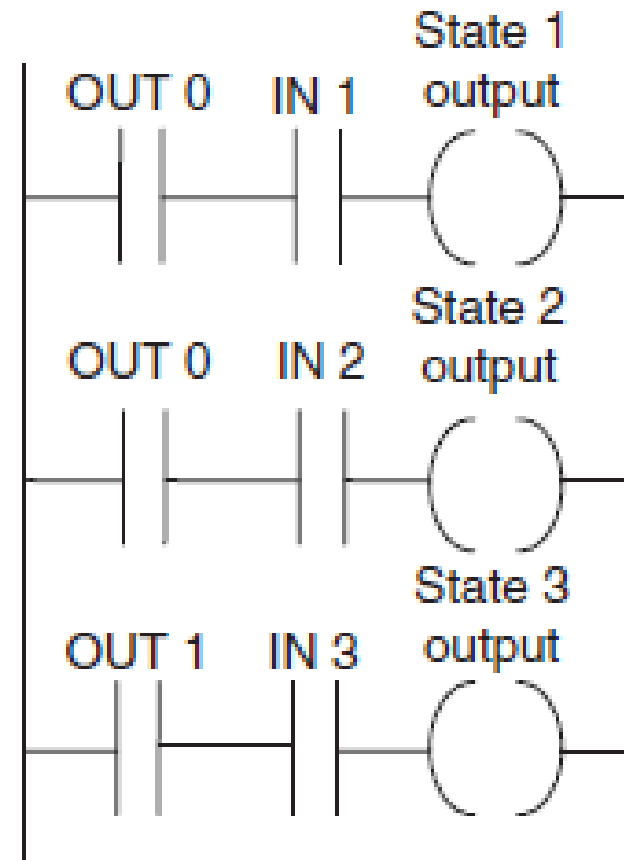


# Branching and Convergence

## Selective Branching



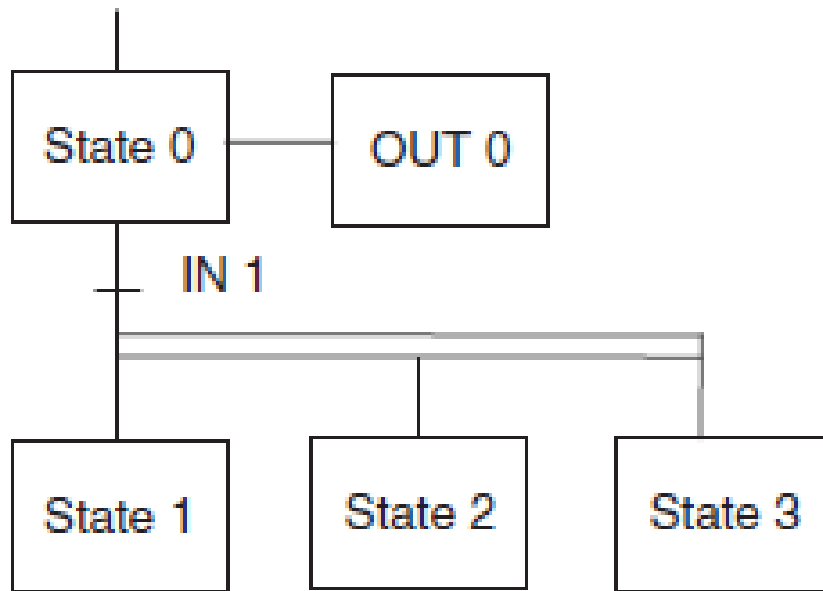
State 1 occurs if the transfer condition IN 1 occurs, but if IN 2 then state 2 or if IN 3 then state 3



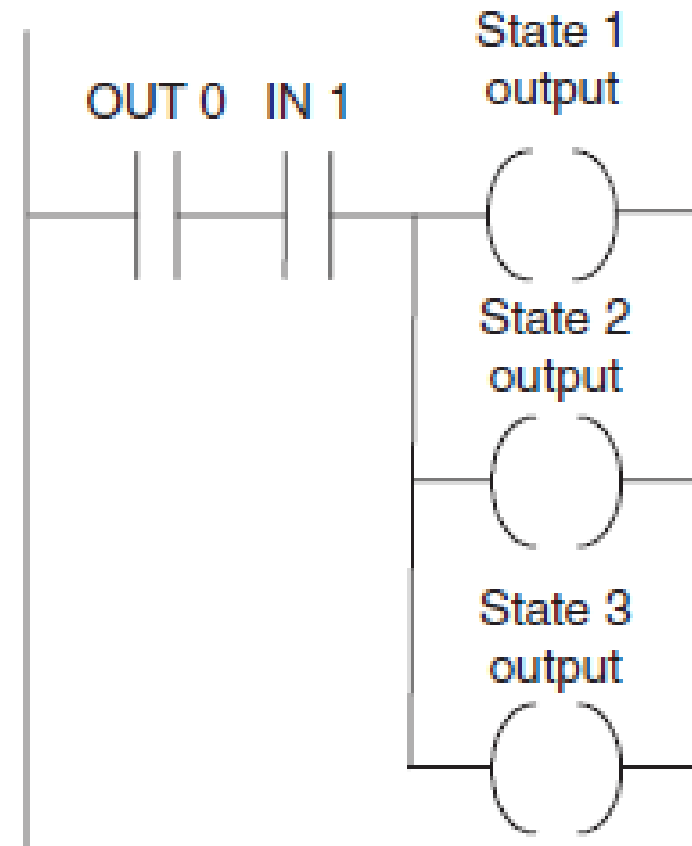


# Branching and Convergence

## Parallel Branching

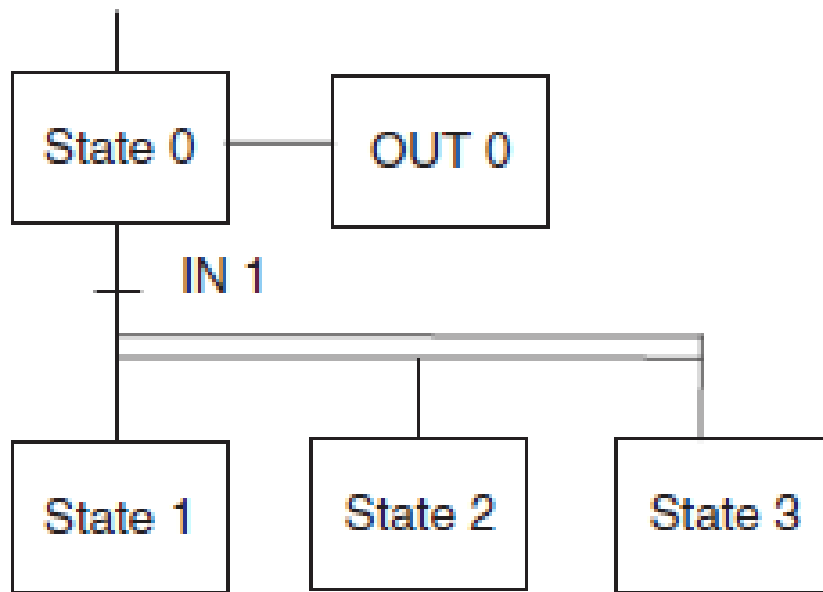


When IN 1 occurs then state 1, state 2 and state 3 are all simultaneously realized.

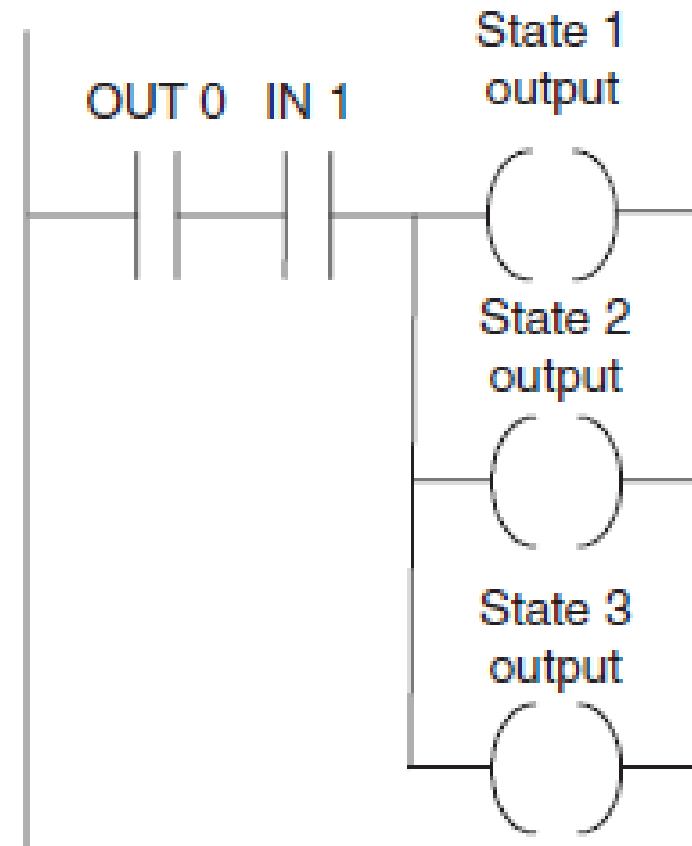


# Branching and Convergence

## Parallel Branching



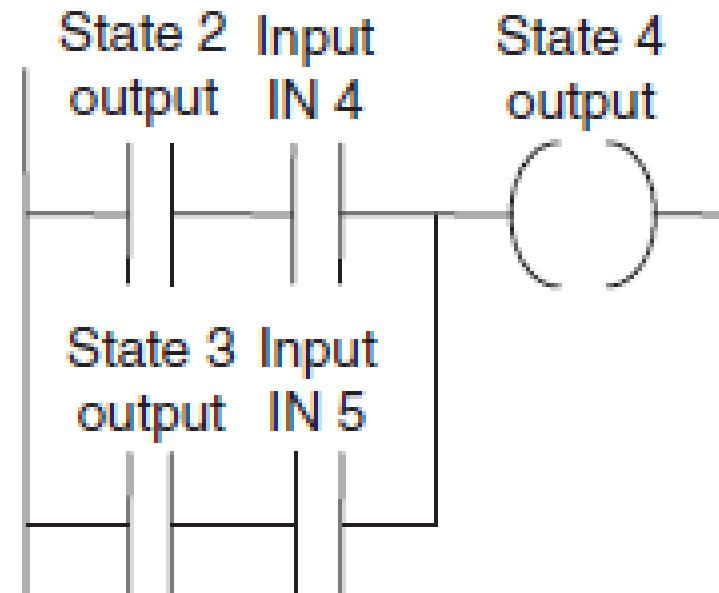
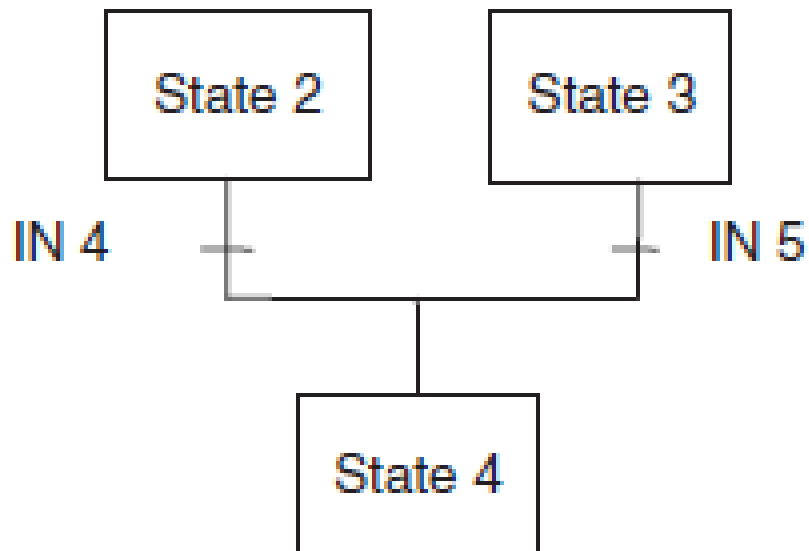
When IN 1 occurs then state 1, state 2 and state 3 are all simultaneously realized.



# Brancing and Convergence

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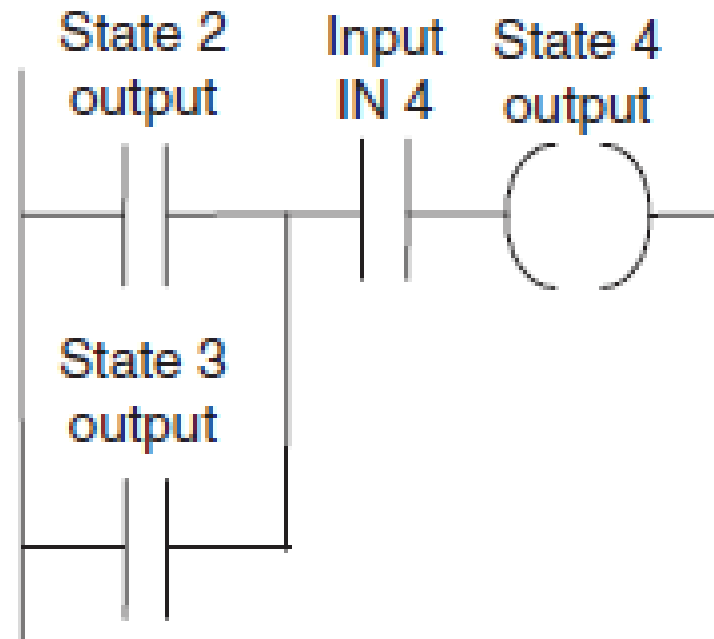
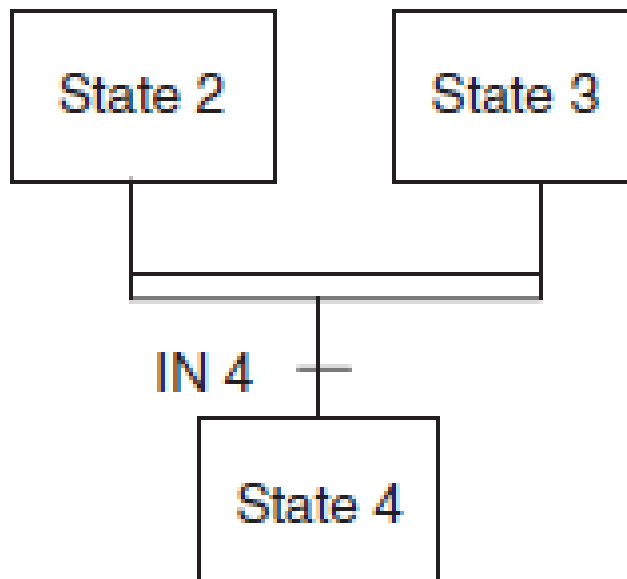
## Convergence



# Branching and Convergence

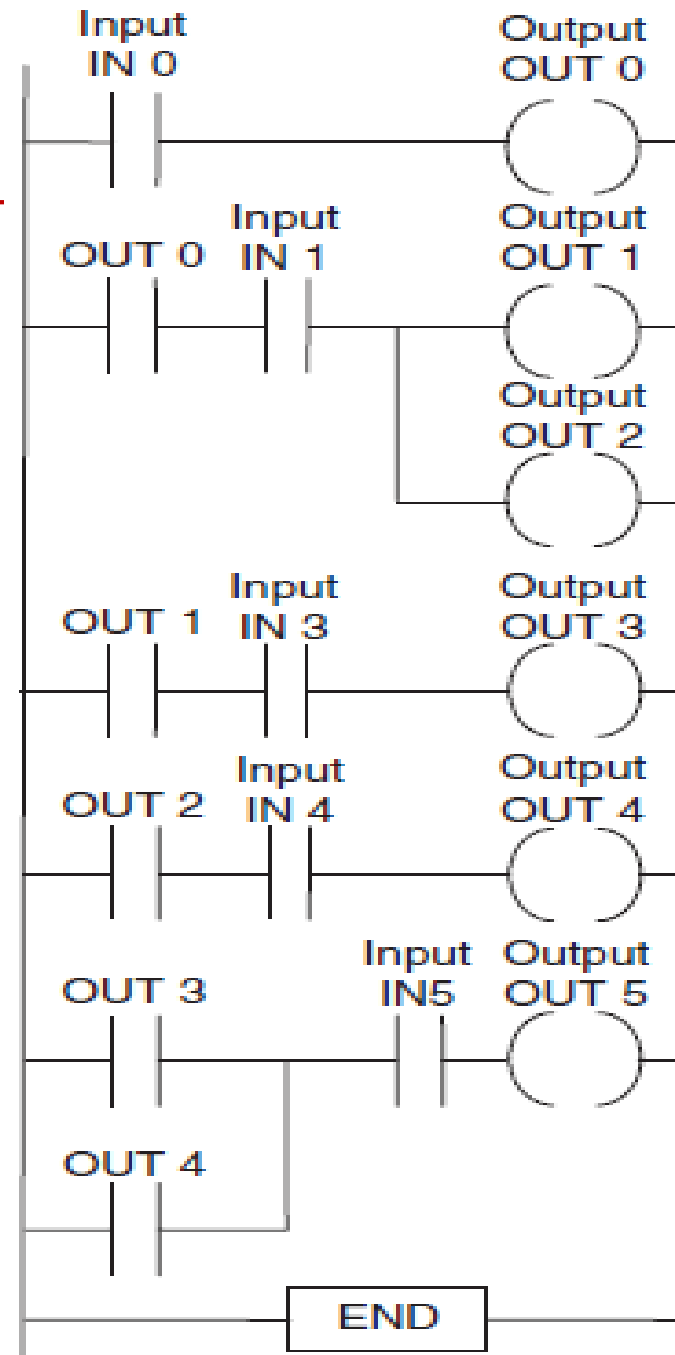
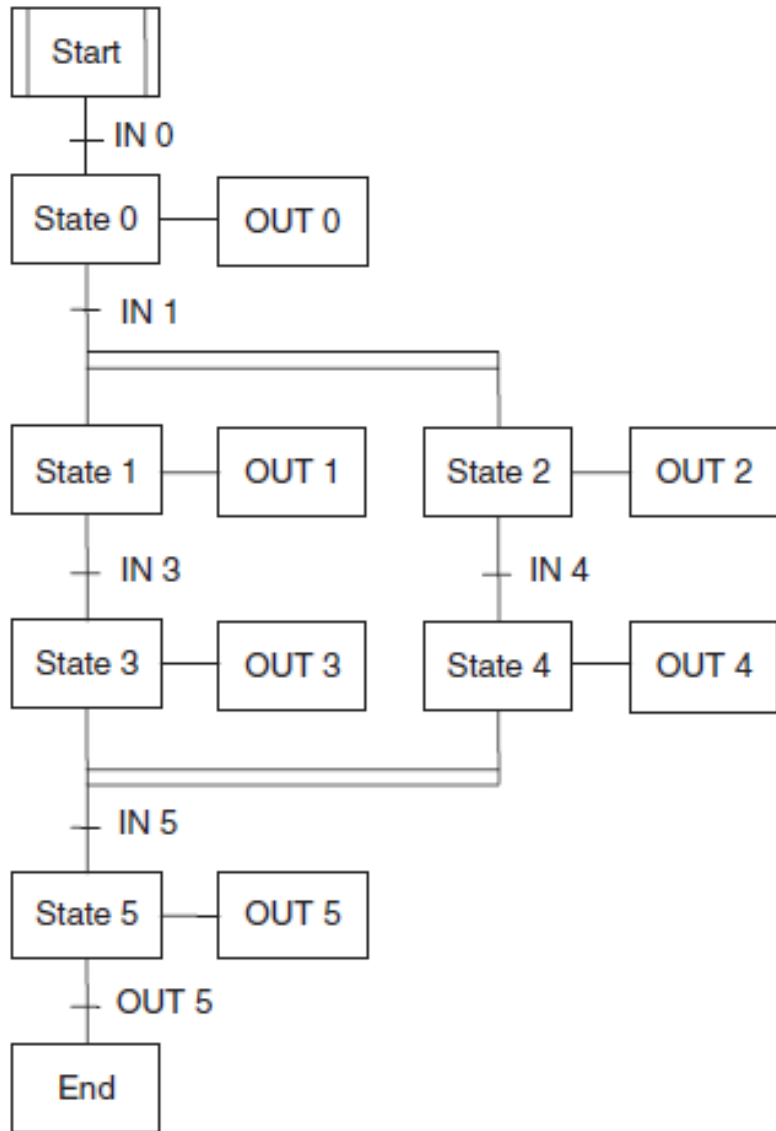
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## Simultaneous Convergence



Simultaneous convergence: When IN 4 occurs State 4 follows from either State 2 or 3.

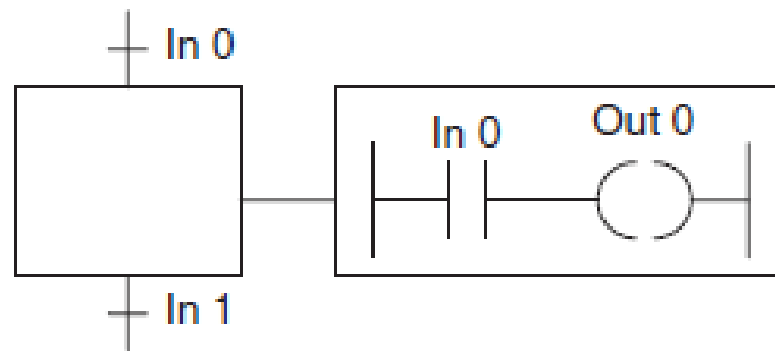
# An illustration



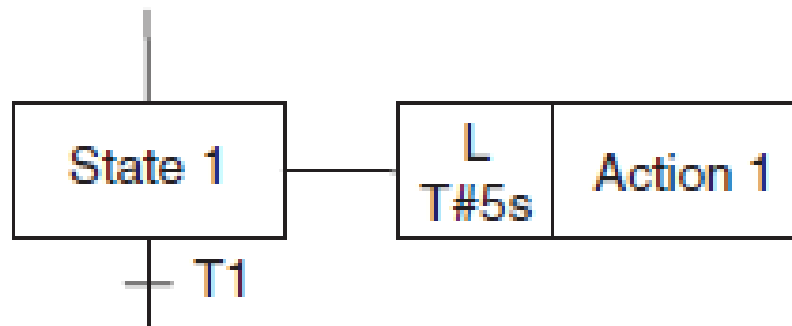
# Action

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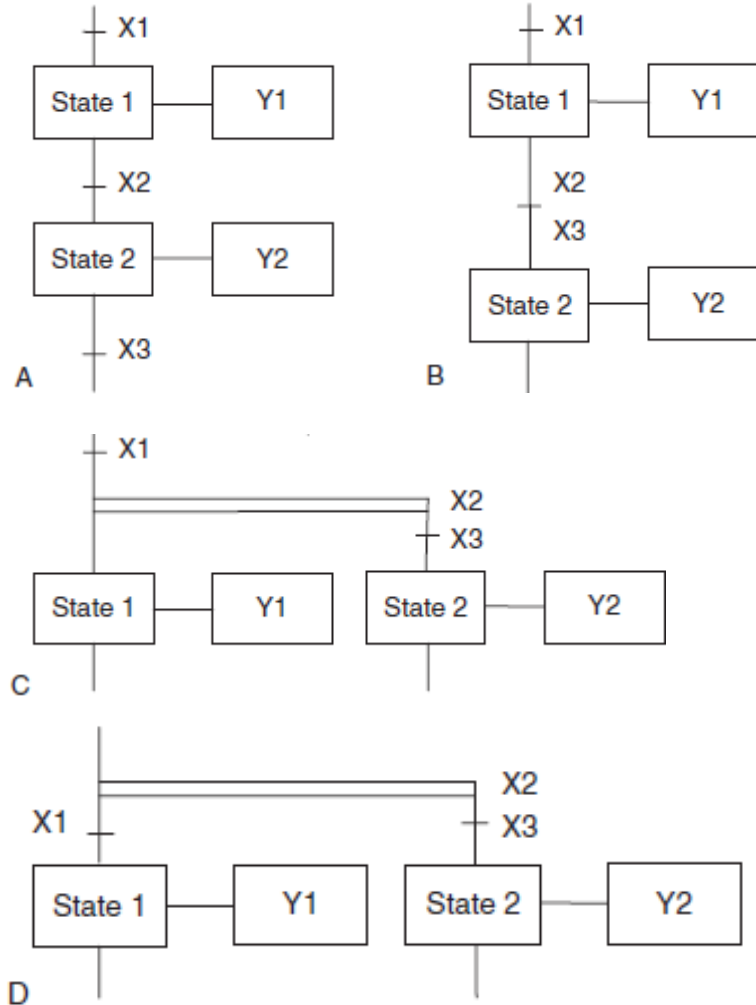
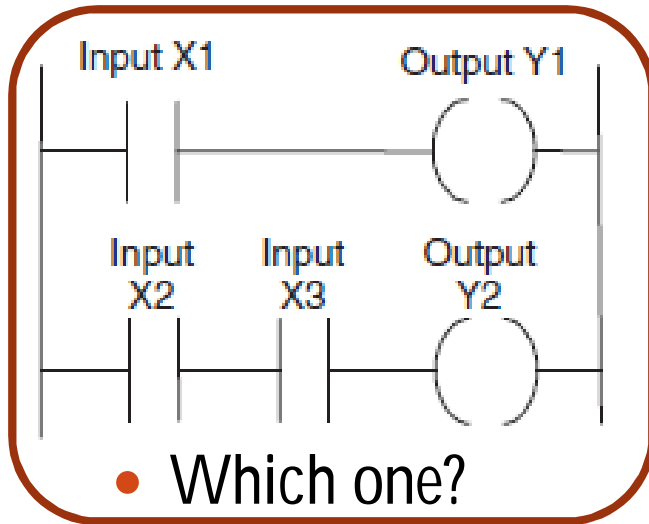
- Action represented by a ladder diagram



- Illustration of a qualifier used with an action, this being a time-limited action

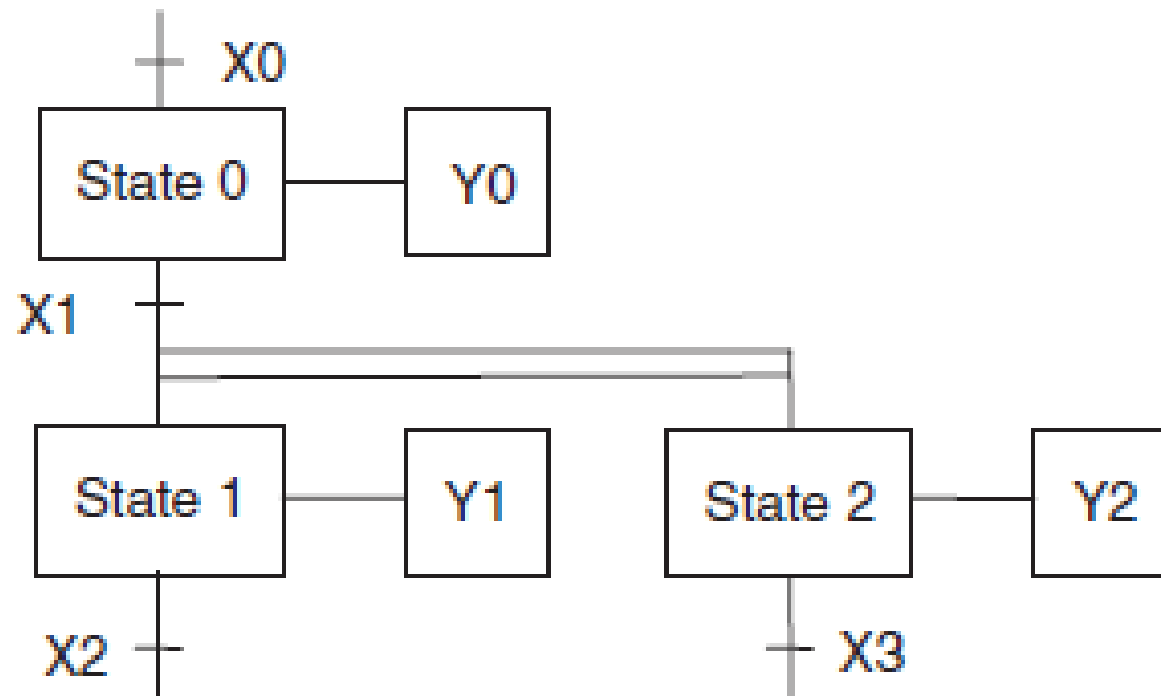


# Problem Case



# Convert to the Ladder Diagram

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# Convert to the Ladder Diagram

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